[TS01W1]

Nanoelectronics 1

Date & Time	July 8(Wed.), 2026 / 09:30-11:30
Place	Room 207
Session Chair(s)	Prof. Seunguk Song (Sungkyunkwan University)

TS01W1_I_1 ***Invited** 09:30-10:00

High-Performance Complementary 2D Transistors and 3D Integration for Advanced Logic

Saptarshi Das

(The Pennsylvania State University)

TS01W1_I_2 ***Invited** 10:00-10:30

Solution-Processed MoS_2 Millimeter-Wave Switches with Sub-Femtosecond Figure-of-Merit for 6G RF Front-Ends

Changwoo Pyo, Sungmoon Park, and Myungsoo Kim

(UNIST)

TS01W1_O_3 10:30-10:45

Strong Light-Matter Interactions in 2D Materials for Optoelectronic Devices

Reitesh Raman, Abhishek Raj, Reyas Ali, Kumar Shwetabh, and Surendra B. Anantharaman

(Indian Institute of Technology Madras)

TS01W1_O_4 ***YSA Applied** 10:45-11:00

Large-Scale Integration of Monolayer Polarity-Reconfigurable Devices for Multifunctional Logic and Optoelectronic Systems

Minh Chien Nguyen and Woo Jong Yu

(Sungkyunkwan University)

TS01W1_O_5 11:00-11:15

Roller-Based Fabrication of High-Quality 2D Metal Oxide Dielectrics from Liquid Metals for High-Performance Electronics

Duong Van Thiet[1] and Luu Van Tho[2,3]

([1]Hanoi University of Industry, [2]Singapore University of Technology and Design, [3]Agency for Science, Technology and Research)

TS01W1_O_6 ***YSA Applied** 11:15-11:30

Screen-Gate Based Organic Transistors for Adaptive Sigmoid and Gaussian Activation Functions

Youngmin Han, Junhyung Cho, Won Woo Lee, and Hocheon Yoo

(Hanyang University)