[TS01T5]

Nanoelectronics 5

Date & Time	July 9(Thu.), 2026 / 15:50-18:05
Place	Room 207
Session Chair(s)	Prof. Hocheon Yoo (Hanyang University)

TS01T5_I_1 ***Invited** 15:50-16:20

In-Sensor Computing for AI-Native Robotic Vision

Changsoon Choi
(Sungkyunkwan University)

TS01T5_I_2 ***Invited** 16:20-16:50

Two-Dimensional Heterostructures for Multi-Functional Electronic Devices Beyond von Neumann Architectures

Yutaka Wakayama
(National Institute for Materials Institute)

TS01T5_I_3 ***Invited** 16:50-17:20

Resistive Switching Materials and Devices for Unconventional Computing

Kyung Seok Woo
(Ulsan National Institute of Science and Technology)

TS01T5_O_4 ***YSA Applied** 17:20-17:35

Synaptic Conductance-Based Device-Aware Attention for Transformers

Seungwoo Baek, Wonwoo Lee, and Hocheon Yoo
(Hanyang University)

TS01T5_O_5 ***YSA Applied** 17:35-17:50

Optimization of Interlayer Engineering in Ferroelectric Tunnel Junctions for Enhanced Reliability with Minimal Polarization Degradation

Junseo Lee, Hongjun Park, and Min-woo Kwon
(Seoul National University of Science and Technology)

TS01T5_O_6 ***YSA Applied** 17:50-18:05

Optically Stimulated Floating-Gate Synapse Transistor for Neuromorphic Computing

Seungme Kang[1], Jisoo Park[2], and Hocheon Yoo[1]
([1]Hanyang University, [2]Gachon University)