[TS01F6]

Nanoelectronics 6

Date & Time	July 10(Fri.), 2026 / 09:30-11:15
Place	Room 207
Session Chair(s)	Prof. Hongseok Oh (Soongsil University)

TS01F6_I_1 ***Invited** 09:30-10:00

2D Materials for Photonic Integrated Circuits

Sejeong Kim
(Sungkyunkwan University)

TS01F6_I_2 ***Invited** 10:00-10:30

Polymer Dielectric Materials for Low-Power Memory and Advanced Computing Devices

Junhwan Choi
(Dankook University)

TS01F6_O_3 ***YSA Applied** 10:30-10:45

High-Temperature Negative Differential Resistance in Tungsten Diselenide Multilayers without Heterojunctions

Hyejin Kim, Seojin Nam, Yunsun Jang, and Min-Kyu Joo
(Sookmyung Women's University)

TS01F6_O_4 ***YSA Applied** 10:45-11:00

BEOL-Compatible Batch Processing for Scalable Integration of Layered 2D SnS_2 Transistors

Seongmo Kang[1,3], Seock-Jin Jeong[1], Byeongjin Park[1], Junwoo Kim[1,2], Dain Kwon[1,2], Mincheol Kim[1,2], Jongwon Yoon[1], Seoung-Ki Lee[3], Yonghun Kim[1,2], and Jiwoo Kim[1]
([1]Korea Institute of Materials Science, [2]Korea Institute of Materials Science, [3]Pusan National University)

TS01F6_O_5 ***YSA Applied** 11:00-11:15

CMOS-Integrated Nanoscale MoS_2 Memristors with Low-Voltage Operation

J. Lee[1], S. Cruces[1], X. Liu[2], Y. Chen[2], V. A. Maroufidis[1], J. Jo[2], L. Völkel[1], D. Braun[1], M. Möller[3], L. Brackmann[1], H. Kalisch[1], M. Heuken[1,4], A. Vescan[1], R. E. Dunin-Borkowski[2], J. Mayer[1,2], S. Wiefels[2], and M. C. Lemme[1,3]
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